IN THE CLAIMS:

Please amend the claims as follows:

1-5. (Cancelled)

6. (Currently amended) A data processing method comprising the steps of:

writing a processing specification information in a first area corresponding to a

first word line within a semiconductor device comprising at least one memory array and

a data processor to said at least one memory array through at least one internal data

bus;

writing data to be processed in a ~~predetermined~~ second area corresponding to a

second word line, which is different from the first word line, within ~~a memory space of a~~

said semiconductor device ~~having a data processing function and serving as a~~

~~memory~~;

transferring said processing specification information through said at least one

internal data bus to said data processor;

transferring said data through said at least one internal data bus to said data

processor;

processing said data by said ~~semiconductor device~~ data processor using said

processing specification information and writing resultant processed data through said

at least one internal data bus in ~~said predetermined area or another predetermined~~

~~area within said room~~ a third area corresponding to a third word line within said

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semiconductor device; and

obtaining said resultant processed data by reading said ~~predetermined area or said another predetermined area within said room of said semiconductor device~~ third area after writing said resultant processed data.

7. (Cancelled)

8. (Currently amended) The data processing method of claim 6 ~~7~~,

wherein said second area and said third area ~~within said room of said semiconductor device~~ are the same area, and

said semiconductor device overwrites said resultant processed data in said second area where said data has been written.

9. (Currently amended) The data processing method of claim 6 ~~7~~, wherein ~~said~~ a controller reads time information required for said processing to be executed, and reads said resultant processed data written in said third area ~~within said room~~ on the basis of said read time information after a time corresponding to said time information elapses.

10. (Original) The data processing method of claim 9, wherein said semiconductor device is connected with said controller through a memory network, and said controller stores time information required for each processing to be executed by said semiconductor device.

11. (Currently amended) The data processing method of claim 6 ~~7, 8, 9 or 10~~,

wherein, immediately before executing said processing by said semiconductor device having the data processing function, information describing said processing to be executed is dynamically rewritten for executing said processing.

12-20. (Cancelled)

21. (Previously added) The data processing method of claim 10 or 13, wherein said memory network has a ring network structure.

22. (Previously added) The data processing method of claim 10 or 13, wherein said memory network has a bus network structure.

23. (New) The data processing method of Claim 6, wherein said data processor comprises reconfigurable logic.

24. (New) The data processing method of Claim 6, wherein said data processor comprises a first data processor portion, a second data processor portion and a register coupled between said first and second data processor portions.